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Sheet 1 of 1

Complete if Known

Application Number	09/994,982
Filing Date	11/26/2001
First Named Inventor:	Xia Dei
Art Unit	2116
Examiner Name	Yanchus III, Paul B.

Attorney Docket Number

42P9724X

U.S. PATENT DOCUMENTS

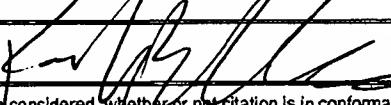
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	US	6,944,480 B1	09-06-2005	Dai	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		JP 11-016668	01-22-1999	Matsushita Electric Ind Co Ltd		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		BELLAOUAR, A., et al., "Supply Voltage Scaling for Temperature Insensitive CMOS Circuit Operation", 1057-7130/98 Copyright 1998 IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pages 415-417.	
		DANCY, Abram, et al., "Techniques for Aggressive Supply Voltage Scaling and Efficient Regulation", 0-7803-3669-0 Copyright 1997 IEEE, IEEE 1997 Custom Integrated Circuits Conference, page 579-586.	
		GONZALEZ, Ricardo, et al., "Supply and Threshold Voltage Scaling for Low Power CMOS", 0018-9200/97 Copyright 1997 IEEE, IEEE Journal of Solid-State Circuits, Vol. 32, No. 8, August 1997, pages 1210-1216.	

Examiner Signature		Date Considered	7/14/06
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